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RESPONSE UNDER 37 C.F.R. § 1.116
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To: Commissioner for Patents

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Facsimile Number: 571-273-8300

Transmission Date: October 18, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tseng

Docket No.: TSM03-0763

Serial No: 10/828,884

Art Unit: 2823

Date Filed: April 21, 2004

Examiner: Khiem D. Nguyen

Title: Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process

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Respectfully submitted,

Judy A. Betts
Legal Assistant

Confirmation Respectfully Requested

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.116

Dear Sir:

The following remarks are presented in response to the Examiner's Office Action mailed September 6, 2006.